

## PATENT ABSTRACTS OF JAPAN

(11)Publication number : 09-321813

(43)Date of publication of application : 12.12.1997

(51)Int.Cl.

H04L 27/18

H04L 1/00

H04L 29/06

(21)Application number : 08-133369

(71)Applicant : NIPPON HOSO KYOKAI &lt;NHK&gt;

(22)Date of filing : 28.05.1996

(72)Inventor : HASHIMOTO AKINORI

KATO HISAKAZU

KIMURA TAKESHI

UEHARA MICHIIRO

NANBA SEIICHI

MATSUMURA HAJIME

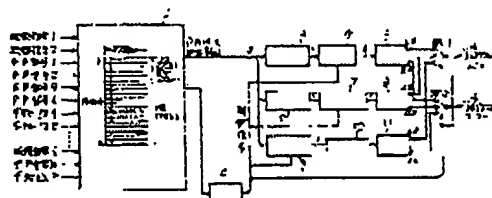
YAMAZAKI SHIGERU

## (54) DIGITAL TRANSMISSION METHOD AND TRANSMITTER-RECEIVER

## (57)Abstract:

PROBLEM TO BE SOLVED: To select a transmission system provided with optimum transmission efficiency and reliability every time by multiplexing signals for indicating a selected parameter to main signals for transmitting program contents and transmitting them.

SOLUTION: Plural video signals, audio signals and data service signals are multiplexed in a multiplexer 1 and outputted. By using the signals, control signals are generated in a control signal generation circuit 2. The output of the multiplexer 1 is converted to the stream of 2 bits or 1 bit in parallel/serial conversion circuits 3. 6 and 9 and passed through encoders 4 and 7, a FIFO 10 and mappers 5, 8 and 11 and multiplexed I signals and Q signals are obtained by changeover switches SW-1 and SW-2. Time division multiplexed modulation waves are obtained from the I signals and the Q signals and supplied to the high frequency stage of a transmitter.



## LEGAL STATUS

[Date of request for examination] 25.02.1998

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number] 3253524

[Date of registration] 22.11.2001

[Number of appeal against examiner's decision  
of rejection]

[Date of requesting appeal against examiner's  
decision of rejection]

[Date of extinction of right]

Copyright (C); 1998,2003 Japan Patent Office